

Shreyasee Dey

EDUCATION

Pursuing MS in the Department of Electrical Engineering(Microelectronics)

University of Texas at Dallas, Richardson, Texas

Courses Taken: Submicron Device Physics and Technology, Analysis and Design of Digital Integrated Circuits, High Speed Computer Arithmetic, VLSI Design, Fields and Waves

Master of Electronics and Telecommunication Engineering with Specialization in Electron Devices

Jadavpur University, Kolkata, India

Courses Taken: Microelectronic Technology, IC Design, Discrete Devices, Microwave Devices, Physical Electronics, Operating System, Advanced logic and Computer Organization, Optoelectronics

Bachelor of Engineering in Electrical Engineering

University College of Engineering, Burla, Orissa, India

Courses Taken: Introduction to Digital Computer and Computer Programming, Instrumentation, Circuit Theory, Electronic Circuits, Digital Electronics, Power Electronics, Microprocessor, Electromagnetic Theory, Control System Engineering

WORK EXPERIENCE

University of Texas at Dallas, Richardson

Graduate Research Assistant, *Jan 2007-present*

Primarily working on the fabrication and characterization of strained device structures and automating characterization tools using NI Labview

Jadavpur University, Kolkata, India

Teaching Assistant, *Sept 1999 – April 2001*

Microprocessor Lab

Tata Consultancy Services Ltd, Chennai, India.

System Tester, *May 2004 – Nov 2004*

Preparation of system test cases, test data and XML scripts for creating test environments.

United Services Automobile Association, San Antonio, TX and

Tata Consultancy Services Ltd, Chennai, India.

System Tester, *Jan 2003 - April 2004*

System Testing and Maintenance on IBM/OS 390 platform involving executing Batch Jobs to test the enhancements and defects before moving into production , solving job abends, making oncalls, modifying production JCLs and preparing and implementing new releases in test. Also responsible for converting existing FOCUS Jobs into Easytrieve and COBOL and testing and validating the converted codes and outputs.

Tata Consultancy Services Ltd, Chennai, India.

Software Engineer, *Oct 2001 – Dec 2002*

Euro currency analysis and conversion on IBM/OS 390 platform using COBOL, JCL programming languages and TCS proprietary testing tools CASEPAC and EAT.

Tata Consultancy Services Ltd, Chennai, India.

Software Engineer, *April 2001 – Sept 2001*

Designing and testing screen prototypes using IBM VisualAge for Java and IBM Websphere Studio.

SKILLS

- **Semiconductor device processing and cleanroom skills :**
 - Cleanroom tools for MOS processes like photolithography
 - Electrical characterization and automation using NI Labview, Semiconductor Paramater Analyzer HP4155, Cascade automated probe station
- **Computation and Simulation skills :**
 - **Design CAD tools:** Cadence, Nanosim, PSPICE, HSPICE
- **Computer skills:**
 - **Operating Systems :** IBM Mainframes(OS 390), UNIX
 - **Programming Languages:** C, C++, COBOL, JCL, FOCUS, EASYTRIEVE, SQL, XML, Web Programming Languages(HTML, JSP),VB
 - **Tools:** TSO/ISPF, IBM Visual Age for JAVA, IBM Websphere Studio
 - **Databases:** DB2

Honors/Activities: Awarded for high percentile in Graduate Aptitude Test for Engineers,1999
Literacy and Healthcare Campaign, Rotaract Club of India, 1994 - 1998
National Social Service, Literacy Program for Underprivileged Children of India, 1990-1992

List of Publications:

[1] Microcontroller based simulation of Fuzzy combinational functions, Proc. (Abstract) VIIIth West Bengal State Science & Technological Congress, pp17,Kalyani,Feb/Mar,2001

[2] MS-Excel spreadsheet simulation of Priority Encoder and its algorithmic expansion,Proc.(Abstract) VIIIth West Bengal State Science & Technological Congress,pp18,Kalyani,Feb/Mar,2001