Dinesh Bhatia is a faculty member in the Erik Jonsson School of Engineering and Computer Science at the University of Texas at Dallas. He directs research activities within the Embedded and Adaptive Computing group. He holds Bachelors in Electrical Engineering and Masters and Ph.D. in Computer Science. He is a senior member of IEEE, Computer Society, Engineering in Medicine and Biology Society, Circuits and Systems Society, Eta Kappa Nu, and recently served on the editorial board of IEEE Transactions on COMPUTERs. He was IEEE Circuits and Systems society's distinguished lecturer for 2007-2008. He was the **General Chair** for the most prestigious International Conference on Body Sensor Networks for 2011. The conference addresses many mechanism for sensing and reporting conditions around human body using low power wireless networks that rely on protocols like Zigbee, Bluetooth, Low energy Bluetooth and many more.

He is one of the foremost authorities in the field of Architecture and Computer Aided Design for low power Field Programmable Gate Arrays (FPGAs). Since 1990, he has published extensively in the related research. He has lead the field and has served on technical program committees of several international conferences related to FPGAs, field programmable technology, and system level design using FPGAs.

Today, he is also working and collaborating on projects that deal with patient monitoring in ICU, hospital, home, and remote locations. He is architect of several patient monitoring solutions that gather patient's vital and contextual data using low power wireless solutions and transmit them to central facilities for storage, analysis, and assisting in decisions for caregivers and healthcare providers. He works closely with many healthcare related establishments that include area hospitals (University of Texas Southwestern Medical School and its hospitals, Texas Health Resources' group of hospitals), clinical researchers, health IT and medical IT providers, and device and technology developers.

In addition, he is working on medical device interoperability, disease management solutions, and has developed technologies that are migrating towards standard solutions. He is also researching and is constantly exploring disruptive technologies for next generation living, energy, and healthcare environments.

He has extensive experience in building large scale custom computing and embedded and systems. Some of these activities include principal designer and investigator for systems for Wright Laboratories of USAF, principal investigator for DARPA funded REACT program, and several more. He has collaborated on phase 1 and phase 2 SBIR programs to build product prototypes. He has

published extensively in leading journals and conferences and continues to serve on program committees of several conferences.

He is experienced in litigations related to patient infringement and has worked with distinguished law. He is founder of Cirasys Corporation.