

**EE7329-501**  
**Homework # 0**

Due: Wednesday, January 17

Download the SPICE inputfile on this line of the webpage. Do not change the design either in topology or component sizes. Run SPICE on the file with the 5V CMOS models. For those of you using SPICE at UTD it will work as is. If you are using HSpice elsewhere you will have to make small modifications to the inputfile so that it is compatible with HSpice formats. After you run SPICE print out the .prt file. Run Simgraph and plot the .pun file, then print out the plot. Turn in this page with your name on it along with the plot and print file all stapled together.